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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Ashraf W. Lotfi, et al.

Serial No.: 09/448,856

Filed: November 23, 1999

For: SiC NMOSFET FOR USE AS A POWER SWITCH AND
A METHOD OF MANUFACTURING THE SAME

Group No.: 2811

Examiner: Ori Nadav

Commissioner of Patents
and Trademarks
Washington, D. C. 20231

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner of Patents and Trademarks, Washington, D.C. 20231, on 08/04/2001 (Date)
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08/04/2001
(Date of Signature)

Sir:

INFORMATION DISCLOSURE STATEMENT

Pursuant to the duty of disclosure under 37 C.F.R. § 1.56, Applicant submits this statement. This submittal is made in accordance with 37 C.F.R. §§ 1.97 and 1.98 and § 609 of the Manual of Patent Examining Procedure. The patents, publications and other information herein are listed below and on the attached Form PTO-1449. Copies of the listed references are submitted herewith.

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Payton

<u>U.S. Patent No.</u>	<u>Inventor</u>	<u>Date</u>
5,528,482	Rozman	06/18/1996
5,331,533	Smith	07/19/1994
5,309,344	Smith	05/03/1994
4,441,146	Vinciarelli	04/03/1984

<u>Foreign Patent No.</u>	<u>Country</u>	<u>Date</u>
WO 93/24987	PCT	12/09/1993

Applicant hereby expressly reserves the right to swear behind the effective dates of any of the above Patents and to question the relevance and materiality of the Patents and Publications listed herein, in whole, in part, or in combination, subsequent to filing this Information Disclosure Statement.

Respectfully submitted,

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